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intellectual property block

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IP block

(Intellectual Property block) See [semiconductor IP](#).

Wikipedia



intellectual property block

In [electronic design](#) and [electronic design automation](#) an **intellectual property block**, **IP-block** or **IP core** is a unit of reusable design, [licensed](#) from a [third party](#). Since the reusable design is [copyrighted](#) and possibly also covered by some [patents](#), licensing such a design involves the transfer of [intellectual property](#), hence the name.

The IP-block is typically used much in the style and manner of a [discrete integrated circuit](#) on a [printed circuit board](#), where the "circuit board" is a larger [ASIC](#) or [FPGA](#) design.

The IP-block commonly takes the form of a computer program written in some [hardware description language](#) such as [Verilog](#), [VHDL](#) or [SystemC](#).

An IP-block can be described as being for [hardware](#) design what a [library](#) is for [computer programming](#).

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Intellectual Property

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Copyright law, patent law and trademark law information.
Find-legal.info

Intellectual Property Law

Everything a laymen needs to know about IP law, from the basics up.
[www.IPEsqire.org](#)

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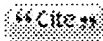


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